

SN54AHC245, SN74AHC245 OCTAL BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

SCLS230H – OCTOBER 1995 – REVISED OCTOBER 2000

- Operating Range 2-V to 5.5-V V_{CC}
- Latch-Up Performance Exceeds 250 mA Per JESD 17

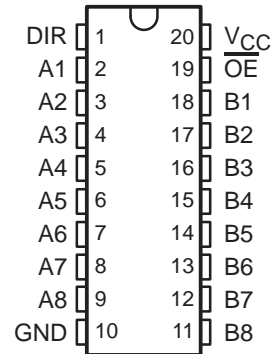
description

The 'AHC245 octal bus transceivers are designed for asynchronous two-way communication between data buses. The control-function implementation minimizes external timing requirements.

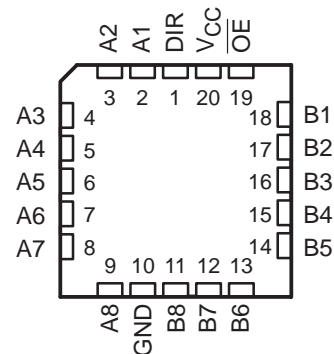
These devices allow data transmission from the A bus to the B bus or from the B bus to the A bus, depending on the logic level at the direction-control (DIR) input. The output-enable (\overline{OE}) input can be used to disable the device so that the buses are effectively isolated.

To ensure the high-impedance state during power up or power down, \overline{OE} should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

SN54AHC245 . . . J OR W PACKAGE
SN74AHC245 . . . DB, DGV, DW, N, OR PW PACKAGE
(TOP VIEW)



SN54AHC245 . . . FK PACKAGE
(TOP VIEW)



ORDERING INFORMATION

T_A	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	PDIP – N	Tube	SN74AHC245N	AHC245
	SOIC – DW	Tube	SN74AHC245DW	
		Tape and reel	SN74AHC245DWR	
	SSOP – DB	Tape and reel	SN74AHC245DBR	HA245
	TSSOP – PW	Tape and reel	SN74AHC245PWR	HA245
-55°C to 125°C	TVSOP – DGV	Tape and reel	SN74AHC245DGV	HA245
	CDIP – J	Tube	SNJ54AHC245J	SNJ54AHC245J
	CFP – W	Tube	SNJ54AHC245W	SNJ54AHC245W
	LCCC – FK	Tube	SNJ54AHC245FK	SNJ54AHC245FK

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**TEXAS
INSTRUMENTS**

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On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

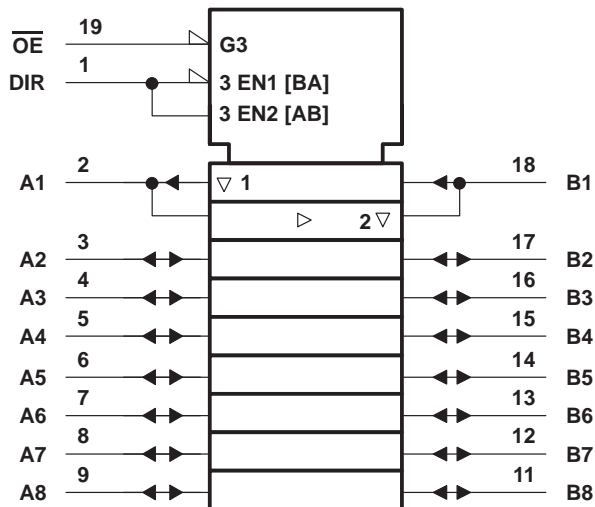
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FUNCTION TABLE (each transceiver)

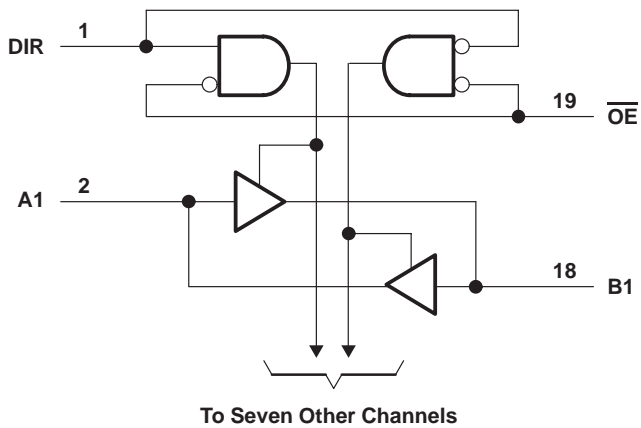
INPUTS		OPERATION
OE	DIR	
L	L	B data to A bus
L	H	A data to B bus
H	X	Isolation

logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



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Supply voltage range, V_{CC}	−0.5 V to 7 V
Input voltage range, V_I (see Note 1): Control inputs	−0.5 V to 7 V
I/O, Output voltage range, V_O (see Note 1)	−0.5 V to $V_{CC} + 0.5$ V
Input clamp current, I_{IK} ($V_I < 0$): Control inputs	−20 mA
I/O, Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$)	±20 mA
Continuous output current, I_O ($V_O = 0$ to V_{CC})	±25 mA
Continuous current through V_{CC} or GND	±75 mA
Package thermal impedance, θ_{JA} (see Note 2): DB package	70°C/W
DGV package	92°C/W
DW package	58°C/W
N package	69°C/W
PW package	83°C/W
Storage temperature range, T_{stg}	−65°C to 150°C

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. The package thermal impedance is calculated in accordance with JEDEC 51-7.

			SN54AHC245		SN74AHC245		UNIT
			MIN	MAX	MIN	MAX	
V _{CC}	Supply voltage		2	5.5	2	5.5	V
V _{IH}	High-level input voltage	V _{CC} = 2 V	1.5		1.5		V
		V _{CC} = 3 V	2.1		2.1		
		V _{CC} = 5.5 V	3.85		3.85		
V _{IL}	Low-level input voltage	V _{CC} = 2 V	0.5		0.5		V
		V _{CC} = 3 V	0.9		0.9		
		V _{CC} = 5.5 V	1.65		1.65		
V _I	Input voltage	$\overline{\text{OE}}$ or DIR	0	5.5	0	5.5	V
V _O	Output voltage	A or B	0	V _{CC}	0	V _{CC}	V
I _{OH}	High-level output current	V _{CC} = 2 V	−50		−50		μA
		V _{CC} = 3.3 V ± 0.3 V	−4		−4		mA
		V _{CC} = 5 V ± 0.5 V	−8		−8		
I _{OL}	Low-level output current	V _{CC} = 2 V	50		50		μA
		V _{CC} = 3.3 V ± 0.3 V	4		4		mA
		V _{CC} = 5 V ± 0.5 V	8		8		
Δt/Δv	Input transition rise or fall rate	V _{CC} = 3.3 V ± 0.3 V	100		100		ns/V
		V _{CC} = 5 V ± 0.5 V	20		20		
T _A	Operating free-air temperature		−55	125	−40	85	°C



SN54AHC245, SN74AHC245

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	V _{CC}	T _A = 25°C			SN54AHC245		SN74AHC245		UNIT
			MIN	TYP	MAX	MIN	MAX	MIN	MAX	
V _{OH}	I _{OH} = -50 µA	2 V	1.9	2		1.9		1.9		V
		3 V	2.9	3		2.9		2.9		
		4.5 V	4.4	4.5		4.4		4.4		
	I _{OH} = -4 mA	3 V	2.58			2.48		2.48		
	I _{OH} = -8 mA	4.5 V	3.94			3.8		3.8		
V _{OL}	I _{OL} = 50 µA	2 V			0.1		0.1		0.1	V
		3 V			0.1		0.1		0.1	
		4.5 V			0.1		0.1		0.1	
	I _{OL} = 4 mA	3 V			0.36		0.5		0.44	
	I _{OL} = 8 mA	4.5 V			0.36		0.5		0.44	
I _I	A or B inputs	V _I = V _{CC} or GND	5.5 V		±0.1		±1		±1	µA
	$\overline{\text{OE}}$ or DIR		0 V to 5.5 V		±0.1		±1*		±1	
I _{OZ} †		V _O = V _{CC} or GND, V _I (OE) = V _{IL} or V _{IH}	5.5 V		±0.25		±2.5		±2.5	µA
I _{CC}		V _I = V _{CC} or GND, I _O = 0	5.5 V		4		40		40	µA
C _i	$\overline{\text{OE}}$ or DIR	V _I = V _{CC} or GND	5 V		2.5 10				10	pF
C _{io}	A or B inputs	V _I = V _{CC} or GND	5 V		4					pF

* On products compliant to MIL-PRF-38535, this parameter is not production tested at V_{CC} = 0 V.

† The parameter I_{OZ} includes the input leakage current.

switching characteristics over recommended operating free-air temperature range, V_{CC} = 3.3 V ± 0.3 V (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	LOAD CAPACITANCE	T _A = 25°C			SN54AHC245		SN74AHC245		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A or B	B or A	C _L = 15 pF		5.8**	8.4**	1**	10**	1	10	ns
t _{PHL}					5.8**	8.4**	1**	10**	1	10	
t _{PZH}	$\overline{\text{OE}}$	A or B	C _L = 15 pF		8.5**	13.2**	1**	15.5**	1	15.5	ns
t _{PZL}					8.5**	13.2**	1**	15.5**	1	15.5	
t _{PHZ}	$\overline{\text{OE}}$	A or B	C _L = 15 pF		8.9**	12.5**	1**	15.5**	1	15.5	ns
t _{PLZ}					8.9**	12.5**	1**	15.5**	1	15.5	
t _{PLH}	A or B	B or A	C _L = 50 pF		8.3	11.9	1	13.5	1	13.5	ns
t _{PHL}					8.3	11.9	1	13.5	1	13.5	
t _{PZH}	$\overline{\text{OE}}$	A or B	C _L = 50 pF		11	16.7	1	19	1	19	ns
t _{PZL}					11	16.7	1	19	1	19	
t _{PHZ}	$\overline{\text{OE}}$	A or B	C _L = 50 pF		11.5	15.8	1	18	1	18	ns
t _{PLZ}					11.5	15.8	1	18	1	18	
t _{sk(o)}			C _L = 50 pF			1.5***				1.5	ns

** On products compliant to MIL-PRF-38535, this parameter is not production tested.

*** On products compliant to MIL-PRF-38535, this parameter does not apply.



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switching characteristics over recommended operating free-air temperature range, $V_{CC} = 5\text{ V} \pm 0.5\text{ V}$ (unless otherwise noted) (see Figure 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	LOAD CAPACITANCE	T _A = 25°C			SN54AHC245		SN74AHC245		UNIT
				MIN	TYP	MAX	MIN	MAX	MIN	MAX	
t _{PLH}	A or B	B or A	C _L = 15 pF	4*	5.5*	1*	6.5*	1	6.5	ns	
t _{PHL}				4*	5.5*	1*	6.5*	1	6.5		
t _{PZH}	OE	A or B	C _L = 15 pF	5.8*	8.5*	1*	10*	1	10	ns	
t _{PZL}				5.8*	8.5*	1*	10*	1	10		
t _{PHZ}	OE	A or B	C _L = 15 pF	5.6*	7.8*	1*	9.2*	1	9.2	ns	
t _{PLZ}				5.6*	7.8*	1*	9.2*	1	9.2		
t _{PLH}	A or B	B or A	C _L = 50 pF	5.5	7.5	1	8.5	1	8.5	ns	
t _{PHL}				5.5	7.5	1	8.5	1	8.5		
t _{PZH}	OE	A or B	C _L = 50 pF	7.3	10.6	1	12	1	12	ns	
t _{PZL}				7.3	10.6	1	12	1	12		
t _{PHZ}	OE	A or B	C _L = 50 pF	7	9.7	1	11	1	11	ns	
t _{PLZ}				7	9.7	1	11	1	11		
t _{sk(o)}			C _L = 50 pF	1**					1	ns	

* On products compliant to MIL-PRF-38535, this parameter is not production tested.

** On products compliant to MIL-PRF-38535, this parameter does not apply.

noise characteristics, $V_{CC} = 5\text{ V}$, $C_L = 50\text{ pF}$, $T_A = 25^\circ\text{C}$ (see Note 4)

PARAMETER		SN74AHC245			UNIT
		MIN	TYP	MAX	
$V_{OL(P)}$	Quiet output, maximum dynamic V_{OL}		0.9		V
$V_{OL(V)}$	Quiet output, minimum dynamic V_{OL}		-0.9		V
$V_{OH(V)}$	Quiet output, minimum dynamic V_{OH}		4.3		V
$V_{IH(D)}$	High-level dynamic input voltage	3.5			V
$V_{IL(D)}$	Low-level dynamic input voltage		1.5		V

NOTE 4: Characteristics are for surface-mount packages only.

operating characteristics, $V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$

PARAMETER		TEST CONDITIONS	TYP	UNIT
C_{pd}	Power dissipation capacitance	No load, $f = 1\text{ MHz}$	14	pF



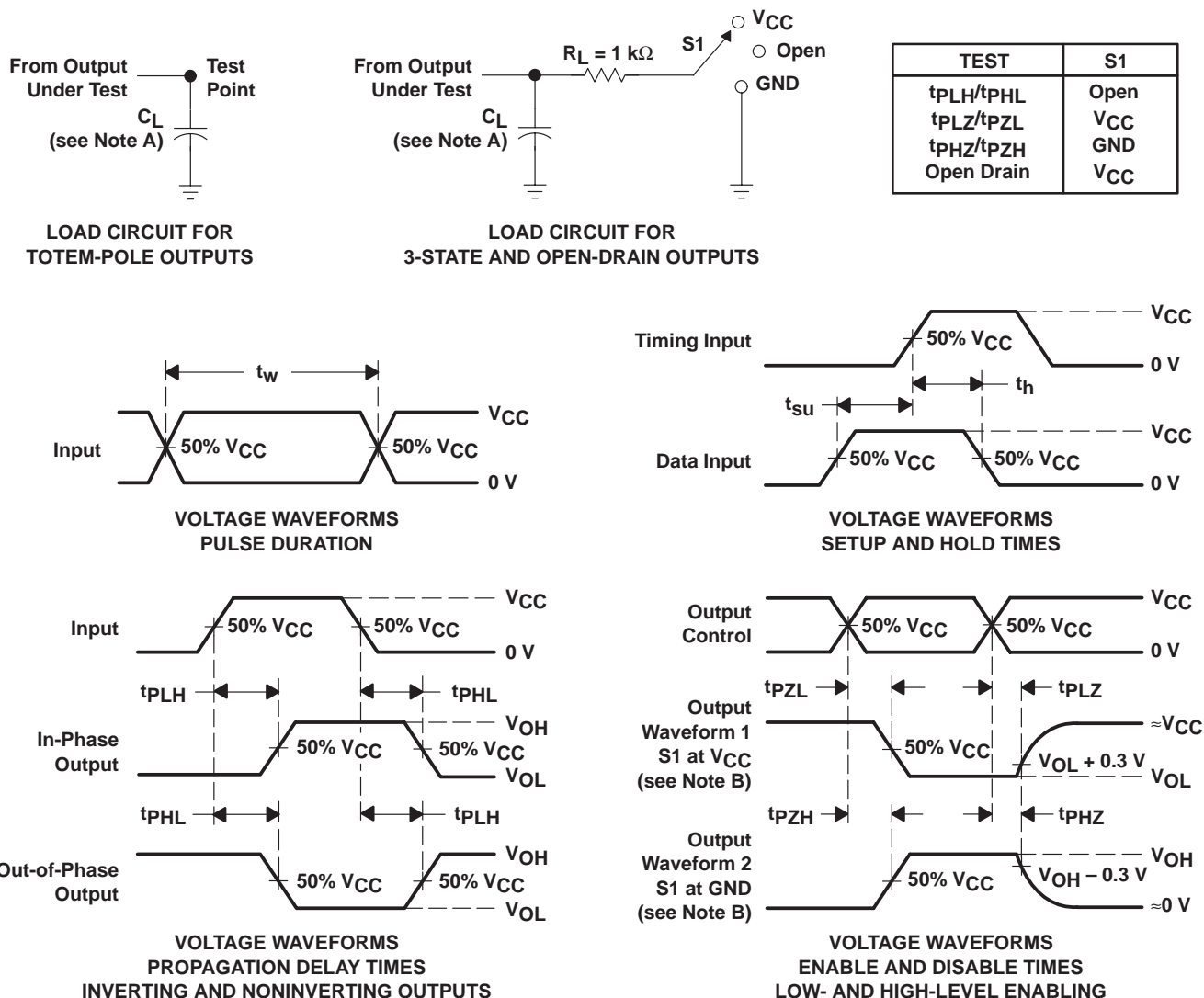
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PARAMETER MEASUREMENT INFORMATION



- NOTES: A. C_L includes probe and jig capacitance.
- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. All input pulses are supplied by generators having the following characteristics: $PRR \leq 1\text{ MHz}$, $Z_O = 50\ \Omega$, $t_r \leq 3\text{ ns}$, $t_f \leq 3\text{ ns}$.
- D. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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